Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3304	713/1,2,100.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 07:36
L2	20	(((multi$1 dual$1) adj port$1) with (processor$1 microcontroller$1)) and management and ((switch$3 select$3) same (processor$1 microcontroller$1)) and (service adj processor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 07:37
L3	22	(((multi$1 dual$1) adj port$1) with (processor$1 microcontroller$1)) and ((switch$3 select$3) same (processor$1 microcontroller$1)) and (service adj processor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 07:44
L4	0	1 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 07:44
L5	0	3 and 719/104.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 07:45
L6	3	3 and 710/104.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 07:48
L7	3	3 and 712/28-37,225,229.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 07:49
S1	34	(dual$1port$1 adj2 (processor$1 microprocessor$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 08:57
S2	662	(dual$1port$1 with (processor$1 microprocessor$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:05

S3	45	S2 and (service$1 adj2 (processor$1 microprocessor$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 08:58
S4	39	S3 and management	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 08:58
S5	11	S4 and (host adj2 processor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 08:59
S6	17	S4 and ((host main) adj2 processor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 08:59
S7	9	(dual$1port$1 with (service adj2 (processor$1 microprocessor$1)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:12
S8	204	(dual$1port$1 with ((service adj2 processor$1) microprocessor$1 micro$1controller$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:12
S9	0	S8 and (remote$1 near3 managem$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:15
S10	43	S8 and ((host main) adj processor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:14
S11	10	(service adj processor$1) and ((host main) adj processor$1) and dual$1port	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:30
S12	59	S8 and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:18

S13	0	S8 and ((mode near3 switch$3) with (service adj processor$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:19
S14	0	S8 and (switch$3 with (service adj processor$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:19
S15	8	(service adj processor$1) same dual$1port	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:19
S16	8	(service adj processor$1) same dual$1port$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:19
S17	41	(service adj processor$1) and ((host main) adj processor$1) and (dual-port (dual adj port))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:42
S18	18	(service adj processor$1) same (dual-port (dual adj port))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 09:42
S19	461	(((multi$1 dual$1) adj port$1) with (processor$1 microcontroller$1)) and management and ((switch$3 select$3) with (processor$1 microcontroller$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 07:36
S20	86	S19 and "712"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/05 09:19
S21	3	S20 and (service adj processor$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/05 09:17